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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				Application Number	
				Filing Date	July 31, 2003
				First Named Inventor	Gerard Chauvel, et al.
				Group Art Unit	2183
				Examiner Name	
Sheet	1	of	1	Attorney Docket No.	TI-35430

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>2</sup> (if known)				
SG	BA	EP	0 800 137 A		IBM	10/8/97		
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
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SG	CA	L. Lopriore; <i>Line Feich/Prefeich in a Stack Cache Memory, Microprocessors and Microsystems</i> , IPC Business Press Ltd., London, GB, vol. 17, no. 9, 1 Nov. 1993, pgs. 547-555.	
	CB		
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Examiner Signature	<i>Shawn Gu</i>	Date Considered	9/27/2003
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